14/507132

PATENTS

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Eiichi HORITA et al.) Art Unit: Not Yet Assigned
Serial No. Not Yet Assigned (National Phase of PCT/JP2005/015085))) Examiner: Not Yet Assigned
Filed: July 21, 2006)
For: Event-Ordering Certification Method) Attorney Docket No.: 44471/332867

Janie Wilkins

INFORMATION DISCLOSURE STATEMENT

Mail Stop PCT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The citation of information on the attached Forms PTO/SB/08a and b, "List of Art Cited by Applicant" is made pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98. A copy of each non-U.S. patent document listed is enclosed.

Cite Nos. 1-5 of the references listed in the Forms PTO/SB/08a and b were cited in the International Search Report during the international phase of the application identified above. A copy of the International Search Report is also enclosed. Cite Nos. 6-9 were cited in the specification.

Information Disclosure Statement Serial No. Unassigned (National Phase of PCT/JP2005/015085) Page 2 of 2

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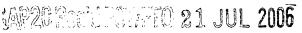
Respectfully submitted,

Bunda Queller Brenda O. Holmes

Reg. No. 40,339

Kilpatrick Stockton LLP 1100 Peachtree Street, Suite 2800 Atlanta, Georgia 30309 (404) 815-6500

KS File: 44471/332867



PTO/SB/08a (08-03)

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Substitute	e for form 1449A/PTC)		Complete if Known			
				Application Number	PCT/JP2005/015085 / 3 4 3		
INFORMATION DISCLOSURE				Filing Date	July 20, 2006		
STATEMENT BY APPLICANT		First Named Inventor	Eiichi HORITA et al.				
				Art Unit			
(Use as many sheets as necessary)		Examiner Name					
Sheet	1	of	2	Attorney Docket Number	44471/332867		

	U.S. PATENT DOCUMENTS						
Examiner Cite		Document Number	Publication Date	Name of Patentee or Applicant of	Pages, Columns, Lines, Where Relevant		
Initials *	Cite No. ¹	Number - Kind Code ² (if known)	MM-DD-YYYY	Cited Document	Passages or Relevant Figures Appear		
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	FOREIGN PATENT DOCUMENTS							
Examiner Cite		Foreign Patent Document	Publication	Name of Patentee or	Pages, Columns, Lines,			
Initials* No.¹ Country Code³ - Number⁴ - Kind Code⁵ (if known)	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	T ⁶					
	1	JP2005-130488	5-19-2005	Nippon Telegraph and Telephone Corporation		x		
	2	JP2005-110225	4-21-2005	Nippon Telegraph and Telephone Corporation		×		
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Substitute for form 1449B/PTO INFORMATION DISCLOSURE				Complete if Known		
				Application Number	PCTUP2005/015085/ 7 4 2	
				Filing Date	July 20, 2006	
STATEMENT BY APPLICANT		First Named Inventor	Eiichi HORITA et al.			
				Art Unit		
(Use as many sheets as necessary)		Examiner Name				
Sheet	2	of	2	Attorney Docket Number	44471/332867	

		NON PATENT LITERATURE DOCUMENTS	,
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	3	MASASHI UNE, KANTA MATSUURA, AKIRA TAKURA, "Digital Timestamp Gijyutsu no Genjyo to Kadai", IMES Discussion Paper Series, Discussion Paper No. 99-J-36, Institute for Monetary and Economic Studies, Bank of Japan, 1999 October, pages 7 to 20, 26 to 28, 40 to 42, http://www.imes.boj.or.jp./idps99/99-J-36.pdf	
	4	EIICHI HOTTA, SATOSHI ONO, and HIDETAKA ISHIMOTO, "Implementation Mechanics of Scalable Event-Ordering System without Single Point of Attack" ("Scalable de Tan'itsu Kogekiten no Nai Ivent Junjo Shomei System Jitsugen Kiko"), The Institute of Electronics, Information and Communication Engineers Gijutsu Kenkyu Hokoku ISEC 2004-85-97, The Institute of Electronics, Information and Communication Engineers, 02 November, 2004, Vol. 104, No. 422, pages 1 to 8	x
	5	SHINGO ORIHARA, SATOSHI ONO, EIICHI HOTTA, and HIDETAKA ISHIMOTO, "Threat analysis of Event- Ordering Systems" ("Ivent Junjo Shomei System no Kyoi Bunseki"), The Institute of Electronics, Information and Communication Engineers Gijyutsu Kenkyu Hokoku ISEC2004-98~105, The Institute of Electronics, Information and Communication Engineers, 10 December, 2004, Vol. 104, No. 527, pages 39 to 45	×
	6	STUART HABER and W. SCOTT STORNETTA, "How to Time-Stamp a Digital Document", Journal of Cryptology, Vol. 3, No. 2, pp. 99-111, 1991	
	7	JJ. QUISQUATER, H. MASSIAS, J. SERRET AVILA, B. PRENEEL, and B. VAN ROMPAY, "Specification and implementation of a timestamping system", Technical Report of Universite Catholique de Louvain, 1999, URL: http://www.dice.ucl.ac.be/crypto/TIMESEC/TR4.tgz	
	8	AHTO BULDAS, HELGER LIPMAA, and BERRY SCHOENMAKERS, "Optimally Efficient Accountable Time-Stamping", in Proceedings of Public Key Cryptography 2000 (PKC2000), eds. Y. Zheng and H. Imai, pp. 293-305, Springer-Verlag, January 2000	
	9	AHTO BULDAS, PEETER LAUD, HELGER LIPMAA, and JAN VILLEMSON, "Time-Stamping with Binary Linking Schemes", in Proceedings of Advances on Cryptology (CRYPTO'98), ed. H. Krawczyk, pp. 486-501, Springer-Verlag, 1998	

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